

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE	
In re application of: Geetha Arthanari, et al.	Date: 06/24/2008
Serial Number: 10/539,334	Examiner: Suchin Parihar
Filed: 06/15/2005	Group Art Unit: 2825
Title: ASIC Clock Floor Planning Method And Structure	IBM Corporation D/18G, B/321, Zip 482 2070 Route 52 Hopewell Junction, NY 12533-6531

**PETITION UNDER 37 CFR 1.313**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

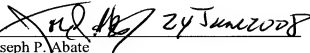
Applicant respectfully requests that the above identified application be withdrawn from issue. Accompanying this Petition is a Request for Continued Examination (RCE) under 37CFR 1.114 and an Information Disclosure Statement (IDS) under 37 CFR 1.156.

The Director is hereby authorized to charge the fee required for this Petition to Deposit Account #090458.

Grant of Applicant's Petition, grant of Applicant's Request for RCE and consideration of the Information Disclosure Statement (IDS) are respectfully solicited.

Respectfully submitted,  
Geetha Arthanari, et al.

By:

  
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